

Packet Digital Elects Technical Advisory Board Member

Fargo, N.D. June 21, 2005: Packet Digital today announced the appointment of Rajesh Gupta to the company's Technical Advisory. Dr. Gupta brings a wealth of scientific, and industry expertise and experience to Packet Digital particularly in low power design and advance power management.

"We are honored to elect Dr. Gupta to our technical advisory board," commented Joel Jorgenson CEO of Packet Digital. "Dr. Gupta is one of the foremost technical leaders in advanced power management and will contribute heavily to Packet Digital technology direction across a multitude of electronics and embedded applications."

Rajesh Gupta is currently a professor of Computer Science and holds the QUALCOMM Chair in Embedded Microsystems at UC San Diego. He received his B. Tech in Electrical Engineering from IIT Kanpur, India, MS in Electrical Engineering and Computer Science from UC Berkeley and a Ph. D. in Electrical Engineering from Stanford University. Dr. Gupta's current research interests are in low power design and mobile computing issues. Earlier he was on the faculty of Computer Science departments at UC Irvine and University of Illinois, Urbana-Champaign. He has worked as a circuit designer at Intel on a number of processor design teams. He is author/co-author of over 150 articles and two books on various aspects of embedded systems, design automation and four patents related to PLL design, datapath synthesis and system-on-chip modeling. Dr. Gupta is a recipient of Chancellor's Fellow at UC Irvine, NSF CAREER Award, departmental achievement and component research team awards at Intel, a best paper award at VLSI design conference and best demonstration award at the Information Processing in Sensor Networks conference. Dr Gupta served as a distinguished lecturer for IEEE Circuits and Systems Society and ACM SIGDA, as a layer leader for CIIT and Telecommunications, chair of IEEE CANDE, founding chair of IEEE/ACM Conference on Models and Methods in Codesign and founding co-chair of IEEE/ACM/IFIP Conference on Codesign and System Synthesis. Dr. Gupta serves as Editor-in-Chief of IEEE Design and Test of Computers, Chair of the Steering Committee on IEEE Transactions on Mobile Computing as well as editorial boards of Transactions on Computer-Aided Design and Mobile Computing. Dr. Gupta is a Fellow of the IEEE.

About Packet Digital

Packet Digital designs, develops, and markets engineering solutions and services that enable higher system performance, miniaturization and integration for a broad range of consumer electronic and computing applications.

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